

## TRANSMITTAL

Electronic Version v1.1

Stylesheet Version v1.1.0

<b>Title of Invention</b>	<b>DYNAMIC SEMICONDUCTOR MEMORY DEVICE AND BIT LINE PRECHARGE METHOD THEREFOR</b>							
Application Number :								
Date :								
First Named Applicant:	Yutaka Nakamura							
Confirmation Number:								
Attorney Docket Number:	JP920030017US1							
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<table border="1"><tr><td>Submitted By:</td><td>Elec. Sign.</td><td>Sign. Capacity</td></tr><tr><td>Michael J. LeStrange Registered Number: 53,207</td><td>/Michael J. LeStrange, Reg. No. 53,207/</td><td>Attorney</td></tr></table>			Submitted By:	Elec. Sign.	Sign. Capacity	Michael J. LeStrange Registered Number: 53,207	/Michael J. LeStrange, Reg. No. 53,207/	Attorney
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Documents being submitted:	Files
us-assignment	JP920030017US1-usassn.xml us-assignment.xsl us-assignment.dtd JP920030017US1assign1.tif JP920030017US1assign2.tif
us-request	JP920030017US1-usrequ.xml us-request.dtd us-request.xsl
us-fee-sheet	JP920030017US1-usfees.xml us-fee-sheet.xsl us-fee-sheet.dtd
application-body	JP920030017US1spec-trans.xml us-application-body.xsl application-body.dtd wipo.ent mathml2.dtd mathml2-qname-1.mod isoamsa.ent isoamsb.ent isoamsc.ent isoamsn.ent isoamso.ent isoamsr.ent isogr3.ent isomfrk.ent isomopf.ent isomscr.ent isotech.ent isobox.ent isocyr1.ent isocyr2.ent isodia.ent isolat1.ent isolat2.ent isonum.ent isopub.ent mmlextra.ent mmlalias.ent soextblx.dtd JP920030017US1Fig1.tif JP920030017US1Fig2.tif JP920030017US1Fig3.tif JP920030017US1Fig4.tif JP920030017US1Fig5.tif JP920030017US1Fig6.tif JP920030017US1Fig7.tif JP920030017US1Fig8.tif JP920030017US1Fig9.tif JP920030017US1Fig10.tif

	JP920030017US1Fig11.tif
	JP920030017US1Fig12.tif
	JP920030017US1Fig13.tif
	JP920030017US1Fig14.tif
us-declaration	JP920030017US1Dec1.tif
us-declaration	JP920030017US1Dec2.tif
<b>Comments</b>	
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